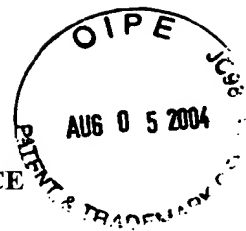


Docket No. 212005US2



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

IN RE APPLICATION OF: Hans Jorgen MATTAUSCH, et al.

SERIAL NO: 09/919,859

GAU: 2188

FILED: August 2, 2001

EXAMINER: Pierre M. VITAL

FOR: MULTI-PORT CACHE MEMORY

**INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97**

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

**REFERENCES**

- ☒ The applicant(s) submit herewith partial translations, listed on the attached form PTO-1449, of two references previously submitted by Information Disclosure Statement filed on June 9, 2004. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

**RELATED CASES**

- ☐ Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

**CERTIFICATION**

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Gregory J. Maier

Registration No. 25,599

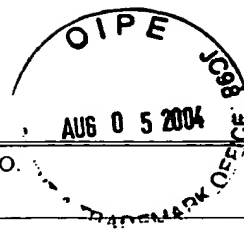
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Form PTO 1449  
(Modified)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.

212005US2

SERIAL NO.

09/919,859

## LIST OF REFERENCES CITED BY APPLICANT

APPLICANT

Hans Jurgen MATTAUSCH, et al.

FILING DATE

August 2, 2001

GROUP

2188

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	AW	SHIMADA Sou, ANDO Hideki and SHIMADA Toshio, "Multi-bank Cache having no Cross-Bus Switch", reports of 2000 Memorial Parallel Processing Symposium (JSPP2000), Information Processing Society of Japan, 30 May 2000, pp. 107-114. <b>Submitting partial translation only (page 107, lines 6-20; page 108, lines 19 - 23 and Figs 1 &amp; 2). Reference previously cited and submitted in prior IDS (6/9/04)</b>
	AX	NATOMI Akira, KUGA Morihiro, MURAKAMI Kazuaki and TOMITA Masaharu, "Load/Store Pipeline of DSN-mode Superscalar Processor Prototype", 24 January 1991, Vol. 91, No. 9, (91-ARC-86), pp. 1-8. <b>Submitting partial translation only (pages 4-7, chapter 4). Reference previously cited and submitted in prior IDS (6/9/04).</b>
	AY	
	AZ	

☐ Additional References sheet(s) attached

Examiner

Date Considered

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.